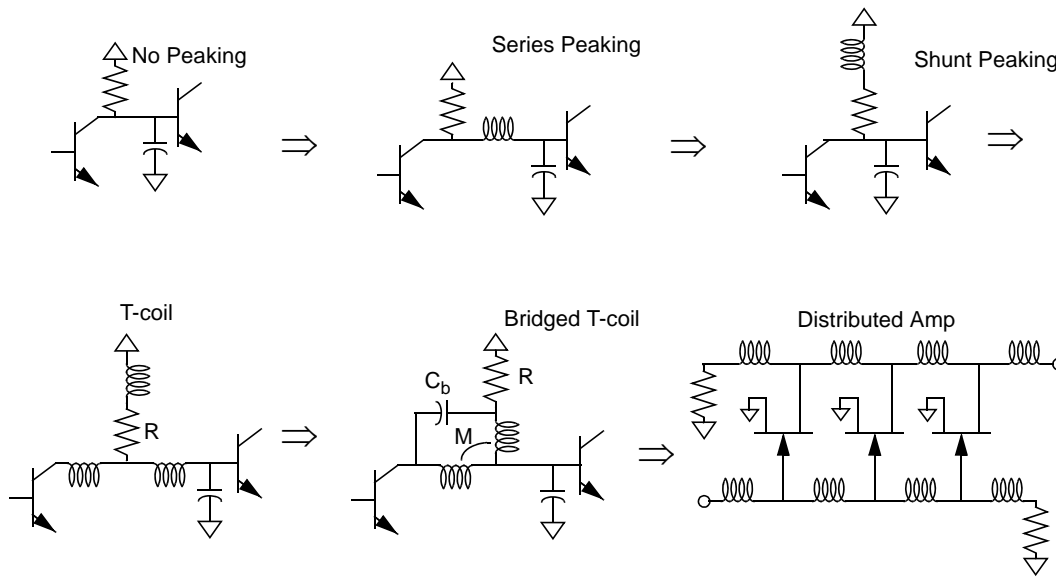


Broadbanding

Progression of circuits with increased bandwidth



This progression shows circuits with added inductors and increasing bandwidth. The underlying philosophy is to separate in time the charging of capacitances and to divert transient current away from the resistor towards the load transistors's input capacitance.

In contrast to the prior pole/zero diagram where the 1st-order “b” term was varied, ensuing pole/zero diagrams reflect pole trajectories associated with varying the inductance, i.e. the “a” term.